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The Global Semiconductor Industry Analysts

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eMMC Holds Its Own Against UFS

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Neuromorphic Chip Gets \$1 Million in Pre-Orders

Neuromorphic computing company GrAI Matter has \$1 million in pre-orders for its GrAI VIP chip. The startup has engagement to date from companies across consumer Tier-1s, module makers (including ADLink, Framos, and ERM), U.S. and French government research, automotive Tier-1s and system integrators, white box suppliers, and distributors.

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Micron to Invest \$40 Billion in U.S. with Passage of CHIPS Act

Micron, the world's third-largest memory chipmaker, said today it plans to invest \$40 billion in expansion in the United States with the passage of the U.S. CHIPS and Science Act. CHIPS stands for "Creating Helpful Incentives to Produce Semiconductors," but the act goes beyond computer components.

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TALK TO US



SEMIKRON and Danfoss Silicon Power become Semikron Danfoss

Less than five months after it was announced in late March, German firms SEMIKRON of Nuremberg and Danfoss Silicon Power GmbH of Flensburg have started doing business as Semikron Danfoss. Semikron Danfoss is owned by the owner-families of SEMIKRON and the Danfoss Group, with Danfoss being the majority owner.

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Nvidia fights back in exascale computing

Nvidia is fighting back in high performance supercomputers, detailing its ninth generation graphics processing unit called Hopper that can be used for exascale (1000PFLOPs) machine learning systems.

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eMMC Holds Its Own Against UFS

The embedded MultiMediaCard (eMMC) standard is no longer being updated. But that's not keeping some vendors from innovating around the NAND flash storage device for use cases where it's still the best option.

While Universal Flash Storage (UFS) was trumpeted as widely replacing eMMC when it debuted, it's overkill for some applications. In an interview with EE Times, Eric Spanneut, vice president of flash global product management for Western Digital's flash business unit, said eMMC works best for mid-range mobile devices and thin-and-light entry-level compute devices, as well as wide range of emerging applications. "eMMC is here to stay for a very long time," he said.

With little fanfare, the company recently introduced its iNAND EM141 Embedded Flash Device, designed for applications that require high performance, reliable data storage, a small form factor, and efficient power consumption.

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The startup has engagement to date from companies across consumer Tier-1s, module makers (including ADLink, Framos, and ERM), U.S. and French government research, automotive Tier-1s and system integrators, white box suppliers, and distributors.

As with previous generations of the company's Neuron Flow core, the company's approach for its GrAI VIP chip uses concepts from event-based sensing and sparsity to process image data efficiently. This means using a stateful neuron design (one that remembers the past) to process only information that has changed between one frame of a video and the next, which helps avoid processing unchanged parts of the frames over and over again. Combine this with a near-memory compute/dataflow architecture and the result is low-latency, low-power, real-time computer vision.

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"CHIPS" stands for "Creating Helpful Incentives to Produce Semiconductors," but the act goes beyond computer components.

U.S. President Joe Biden is expected to sign the act today, which will provide about \$52 billion in subsidies and research grants to semiconductor companies that invest in the nation and limit their expansion in China.

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With an existing workforce of more than 3500, the merged power semiconductor module firm retains the two main locations in Germany. All global subsidiaries, production sites and distribution channels continue.

Picture: Danfoss Silicon Power's general manager Claus A. Petersen (left), who becomes CEO of Semikron Danfoss, and former SEMIKRON CEO Karl-Heinz Gaubatz (right), who becomes CTO until his planned retirement at the end of 2022.

Nvidia Fights Back In Exascale Computing

Nvidia is fighting back in high performance supercomputers, detailing its ninth generation graphics processing unit called Hopper that can be used for exascale (1000PFLOPs) machine learning systems.

At the Hot Chips conference in California, the company has shown more details of its Hopper GPU and how it can be used to build exascale AI supercomputers.

This follows the commissioning of the first US exascale machine, called Frontier, at ORNL. This uses AMD processors and GPUs. Nvidia has been working with European chip designer, SiPearl, on exascale computer designs.