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The Global Semiconductor Industry Analysts

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Dialog 50¢ SoC Targets Disposable Bluetooth

Dialog Semiconductor has announced a new Bluetooth 5.1 system-on-chip (SoC) which it claims can add Bluetooth Low Energy (BLE) to an application for as little as \$0.50, opening up an opportunity in mass-market, disposable, Internet of things (IoT) connected devices.

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6 MEMS and Sensors Startups on Fast Track to

The Internet of things is becoming a reality. Everything is connected, from what we put in our bodies, what we have in our cars, our homes and our cities. Market research firm McKinsey predicts the IoT economic impact to be in the range of about \$4 trillion to \$11 trillion per year in 2025.

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FPGA acceleration card delivers on bandwidth.

High-bandwidth compute applications and diverse workloads are driving the adoption of FPGAs in accelerator cards and how these devices are being consumed. Achronix Semiconductor Corp. and BittWare, a Molex Company, released a new class of FPGA accelerator card for cloud and edge computing, designed for a variety of different workloads

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TALK TO US



eSilicon to Be Split Between Synopsys and Inphi

Inphi Corp., is buying most of eSilicon; while Synopsys will acquire the fabless vendor's embedded memory and interface intellectual property (IP) business. Inphi is to pay \$216 million for eSilicon in both cash and assumption of debt, while the price that Synopsys paid for the memory assets was not disclosed.

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EVENTS

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Blaize Fires up GSP for AI Processing

TOKYO — AI processor designer Blaize, formerly known as ThinCI (pronounced "Think-Eye"), revealed its fully programmable Graph Streaming Processor (GSP) will go into volume production in the second quarter of 2020.

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Dialog 50¢ SoC Targets Disposable Bluetooth Market

Dialog Semiconductor has announced a new Bluetooth 5.1 system-on-chip (SoC) which it claims can add Bluetooth Low Energy (BLE) to an application for as little as \$0.50, opening up an opportunity in mass-market, disposable, Internet of things (IoT) connected devices.

Its new DA14531 chip, which it also calls SmartBond Tiny, makes it possible to extend wireless connectivity to applications where it would have previously been prohibitive in terms of size, power or cost, especially those within the growing connected medical field. SmartBond Tiny is expected to help facilitate connectivity for inhalers, medicine dispensers, weight scales, thermometers, glucose meters and more.

6 MEMS And Sensors Startups On Fast Track to Grow

The Internet of things is becoming a reality. Everything is connected, from what we put in our bodies, what we have in our cars, our homes and our cities. Market research firm McKinsey predicts the IoT economic impact to be in the range of about \$4 trillion to \$11 trillion per year in 2025. MEMS and sensors are an essential part of it, and the question is how to take a bigger chunk of that pie. One answer comes from high-potential startups.

The 2019 MEMS & Sensors Executive Congress (MSEC), organized by SEMI in Coronado Island, Calif., is a can't-miss event to take the pulse of the industry from some of the movers and shakers in the community. Heavily discussed and debated over this two-day conference, the Technology Showcase amplified awareness of the latest MEMS and sensors technologies and applications, including a DNA search engine, a 4D LiDAR for autonomous vehicles, as well as wearable biosensors for healthcare.

FPGA Acceleration Card Delivers On Bandwidth, Speed, And Flexibility

High-bandwidth compute applications and diverse workloads are driving the adoption of FPGAs in accelerator cards and how these devices are being consumed. Achronix Semiconductor Corp. and BittWare, a Molex Company, released a new class of FPGA accelerator card for cloud and edge computing, designed for a variety of different workloads. For customers looking for faster and lower risk deployment, the companies also offer the card as a pre-integrated and fully-tested FPGA server platform.

Targeting high-performance and high-bandwidth compute and data acceleration applications, the VectorPath S7t-VG6 accelerator card features Achronix's 7-nm Speedster7t AC7t1500 FPGA, designed with the industry's highest performance interfaces available on a PCI Express (PCIe) FPGA accelerator card.

eSilicon to Be Split Between Synopsys and Inphi

Inphi Corp., is buying most of eSilicon; while Synopsys will acquire the fabless vendor's embedded memory and interface intellectual property (IP) business. Inphi is to pay \$216 million for eSilicon in both cash and assumption of debt, while the price that Synopsys paid for the memory assets was not disclosed.

eSilicon was established in 2000 and provides complex FinFET ASICs, market-specific IP platforms and advanced 2.5D packaging solutions. Targeting high-bandwidth networking, high-performance computing, artificial intelligence (AI) and 5G infrastructure markets, its IP includes configurable 7nm 56G/112G SerDes plus networking-optimized 16/14/7nm FinFET IP platforms featuring HBM2 PHY, ternary content-addressable memory (TCAM), specialized memory compilers and I/O libraries. Its neuASIC platform provides AI-specific IP and a modular design methodology to create ASICs.

Blaize Fires Up GSP For AI Processing

TOKYO — AI processor designer Blaize, formerly known as ThinCI (pronounced "Think-Eye"), revealed its fully programmable Graph Streaming Processor (GSP) will go into volume production in the second quarter of 2020.

While the six-year-old startup is mum on its product specifications — such as power level and benchmarking results — its test chip, taped out in mid-2018 and housed in a Linux-based box, has been engaged in 16 pilot programs worldwide for a year, claimed Blaize co-founder and CEO Dinakar Munagala.

Blaize describes its GSP as capable of performing "direct graph processing, on-chip task-graph management and execution, and task parallelism." In short, Blaize designed the GSP to fulfill AI processing needs that have been previously unmet by GPU, CPU or DSP.