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The Global Semiconductor Industry Analysts

## FH MONDAY

16 March 2020

### Ceva Pitches New DSP Core

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### Huawei: UK Carriers to Be Hit by Clampdown

The U.K. Government's decision to allow Chinese infrastructure group Huawei to continue supplying equipment in the country, but at a controlled rate, will have significant and lasting implications for many in the industry, even leaving aside the politics and security concerns.

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### ISSCC: Automotive Processors, Chiplets, and 5G

This year's processor session at the ISSCC led off with two presentations by AMD (for the first time) followed by presentations from Samsung and MediaTek talking about their latest 5G smart phone chips, a research project/proof of concept design from CEA technology, an automotive system on chip (SoC) from Texas Instruments (TI), and the latest IBM Z series mainframe processor.

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### TALK TO US



### Hailo Raises \$60m for AI Chip Productization

AI chip startup Hailo, based in Tel-Aviv, Israel, has raised \$60 million in a B-round of funding, bringing the company's total financing to \$88 million. This funding will be used for further productization and commercialization of the company's chip, the Hailo-8, as well as continued development of the company's hardware and software, and to support an increased global presence as the company grows.

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### EVENTS

#### [Silicon Chip Industry Seminar](#)

-15 June 2020– London UK

#### [Industry Forecast Briefing](#)

– 15 Sept 2020 – London UK

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### 5G RF Issues Send Soitec Seeking New Wafer Material

BARCELONA – Despite the Mobile World Congress cancellation, the pursuit of 5G grows fiercer by the hour, especially among electronics players who are hitting silicon performance limits for 5G RF front-end modules.

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## **Ceva Pitches New DSP Core**

PARIS — For telecom equipment vendors, designing the right 5G network gear is a moving target that keeps going higher. As 5G advances from the current New Radio (NR) Phase I to Phase II, the new 5G Radio Access Network demands more transport flexibility and different base station functional splits.

With that in mind, Ceva, a licensor of DSP cores, unveiled Wednesday its newest DSP architecture, called the Gen4 CEVA-XC.

The Gen4 CEVA-XC is “much more than a DSP core,” according to Nir Shapira, business development director, mobile broadband business unit at Ceva. Describing it as “more of a complete compute platform,” he said the new DSP core natively features multithread and multicore architecture. It contains a built-in scheme designed for dynamic-vector computing resource allocation

## **Huawei: UK Carriers to Be Hit by Clampdown**

The U.K. Government’s decision to allow Chinese infrastructure group Huawei to continue supplying equipment in the country, but at a controlled rate, will have significant and lasting implications for many in the industry, even leaving aside the politics and security concerns.

Huawei has also been excluded for gear in the security sensitive ‘core network’ as the country gears up for 5G.

Those with the biggest calls to make include: the country’s mobile and landline network operators — notably BT, Vodafone and Three; Huawei itself as regards its business in the U.K., perhaps its most significant in Europe; and the alternative network gear providers — Nokia, Ericsson, and Samsung.

## **ISSCC: Automotive Processors, Chiplets, and 5G**

SAN FRANCISCO — This year’s processor session at the ISSCC led off with two presentations by AMD (for the first time) followed by presentations from Samsung and MediaTek talking about their latest 5G smart phone chips, a research project/proof of concept design from CEA technology, an automotive system on chip (SoC) from Texas Instruments (TI), and the latest IBM Z series mainframe processor.

And because this conference is primarily a circuit design conference, each vendor focused on one or more specific aspects of circuit design that was unique in their processors.

The International Solid-State Circuit Conference (ISSCC) is one of the longest-running technical conferences in the semiconductor industry; it takes place every February here. The conference has a mix of academic and industry participants to discuss the latest challenges in chip circuit designs.

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Hailo launched its Hailo-8 processor for edge and endpoint devices in May 2019. It uses a novel architecture in which compute, memory and control blocks are mixed together; software allocates adjacent blocks to work on each layer of a neural network, depending on the compute and memory requirements of that layer. It offers 26 TOPS at 2.8 TOPS/W for AI inference acceleration in edge and endpoint devices.

## **5G RF Issues Send Soitec Seeking New Wafer Material**

BARCELONA – Despite the Mobile World Congress cancellation, the pursuit of 5G grows fiercer by the hour, especially among electronics players who are hitting silicon performance limits for 5G RF front-end modules.

Among the candidate materials to supplant silicon are compound materials such as gallium nitride (GaN), gallium arsenide (GaAs), and silicon carbide (SiC), along with piezoelectrics, which are being used to improve filters. GaAs has been used for power amplifiers in 4G and 5G handsets. GaN has begun gaining traction for power amplifiers in 5G mmWave markets.

More and more RF fabless chip companies are seeking “new materials to solve their problems,” Paul Boudre, CEO of Soitec, told EE Times this week in an interview here.