



**Joël Hartmann**  
**Group Vice-President, Technology R&D**  
**Director, Advanced CMOS Logic & Derivative Technologies**  
**STMicroelectronics**

Joël Hartmann is the Group Vice President and Director of Advanced CMOS Logic and Derivative Technologies at STMicroelectronics and has held this position since 2008.

From 1979 to 2000, Hartmann worked at CEA-Leti, a premier applied-research center for microelectronics, information and healthcare technologies in Grenoble, France. He researched X-ray photolithography and later was put in charge of process integration technologies. In 1997, Hartmann was appointed Director of the Microelectronics Department, and he also supervised the collaboration initiative between CEA-Leti and France Telecom in 1998. In 2000, Hartmann joined STMicroelectronics as Director of the Crolles2 Alliance, the large-scale semiconductor manufacturing R&D initiative of STMicroelectronics, NXP (Philips) and Freescale in Crolles, France.

Hartmann has filed 15 patents on semiconductor technology and devices and authored 10 publications in this field to date.

Joël Hartmann was born in Toulon, France, in 1955. He graduated from the Grenoble Institute of Technology (ENSPG) with a degree in Physics.

