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The Global Semiconductor Industry Analysts

FH MONDAY

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Fingerprint Scanner market research report provides the newest industry data and industry future trends, allowing you to identify the products and end users driving Revenue growth and profitability.

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ST to Buy Swedish SiC Wafer Supplier

LONDON — With a global shortage of silicon-carbide (SiC) wafers, STMicroelectronics is looking to ensure that it can meet customer demand by acquiring a majority stake in Swedish SiC wafer manufacturer Norstel AB.

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Infrastructure Focus Clouds Bull View on 6G

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TALK TO US



NVMe Over TCP Will Take Time to Eclipse RDMA

TORONTO — This year is already predicted to be a big one for NVMe-over-Fabric, and NVMe over TCP is expected to be a significant contributor. The NVMe/TCP Transport Binding specification was ratified in November and joins PCIe, RDMA, and Fiber Channel as an available transport

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Uber Calls for AI Standard

SANTA CLARA, Calif. — Deep-learning accelerators need a standard interface, said a top engineering manager at Uber who sketched a picture of the company's use of AI, its data centers, and their challenges.

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Factors Driving The Growth Of Fingerprint Scanner Market In Electronics Industry

arcognition.com has added latest research report on “Global Fingerprint Scanner Market”, this report helps to analyze top manufacturers, regions, revenue, price, and also covers Industry sales channel, distributors, traders, dealers, research findings, conclusion, appendix and data source.

Fingerprint Scanner market research report provides the newest industry data and industry future trends, allowing you to identify the products and end users driving Revenue growth and profitability.

The industry report lists the leading competitors and provides the insights strategic industry Analysis of the key factors influencing the market.

ST to Buy Swedish SiC Wafer Supplier

LONDON — With a global shortage of silicon-carbide (SiC) wafers, STMicroelectronics is looking to ensure that it can meet customer demand by acquiring a majority stake in Swedish SiC wafer manufacturer Norstel AB.

ST will acquire 55% of Norstel's share capital, with an option to acquire the remaining 45% subject to certain conditions. The total value of the deal if the option is exercised would be \$137.5 million in cash.

"We want to build on our strong momentum in SiC, both in volume and breadth of applications for industrial and automotive," said Jean-Marc Chery, ST's president and CEO.

Infrastructure Focus Clouds Bull View On 6G

The uncertainties for the still-nascent 6G come from two sources: Handset fatigue; and regional/political factors of backbone networks.

The 35-year history of digital cellular standards has followed a predictable trajectory since the 3G Partnership Project took the lead in standards development. As soon as one generation of standard reached draft form in 3GPP, it was time to speculate about the next generation, even if the barest framework of the next generation was still years away. The speculation has led to hype and nonsense in 4G and 5G, but for the still-nascent 6G, the decade of the 2020s may be too murky for even the best crystal balls to penetrate.

Uncertainty about 6G comes from two sources. First, with “handset fatigue” dampening end users' enthusiasm for continual upgrades to new smartphone models, the standard will be defined almost solely by upgrades to infrastructure. The soft-function trends of software-defined networking (SDN) and network function virtualization (NFV) will drive base station and remote radio head development. That could make the feature sets of soft switches and SDN controllers seem downright squishy.

NVMe Over TCP Will Take Time To Eclipse RDMA

TORONTO — This year is already predicted to be a big one for NVMe-over-Fabric, and NVMe over TCP is expected to be a significant contributor.

The NVMe/TCP Transport Binding specification was ratified in November and joins PCIe, RDMA, and Fiber Channel as an available transport. A key benefit of the NVMe/TCP is that it enables efficient end-to-end NVMe operations between NVMe-oF host(s) and NVMe-oF controller devices interconnected by any standard IP network. At the same time, it maintains the performance and latency characteristics that enable large-scale data centers to use their existing Ethernet infrastructure and network adapters. It's also designed to layer over existing software-based TCP transport implementations while also ready for future hardware-accelerated implementations.

Uber Calls For AI Standard

SANTA CLARA, Calif. — Deep-learning accelerators need a standard interface, said a top engineering manager at Uber who sketched a picture of the company's use of AI, its data centers, and their challenges.

“AI is really disrupting our industry” across the design of chips, boards, systems, and services, said Gloria Lau, head of hardware engineering at Uber, in a keynote at DesignCon here.

Like many other web giants, Uber uses banks of Nvidia GPUs for deep learning today, often riding Nvidia's NVLink interface. Also like other large data center operators, it is testing FPGAs and ASICs from startups including Eyeris, Graphcore, and Wave Computing in its search for more performance and efficiency.

“I would love to see a standard interface for all AI chips — NVLink is just for Nvidia,” Lau said.