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The Global Semiconductor Industry Analysts

# **FH MONDAY**

Startups Reinventing Wi-Huawei Rolls 7-nm Arm Nvidia Turing GPU Brings Server CPU Fi for IoT Ray-Tracing in Real Time Huawei announced a 7-nm SAN FRANCISCO - Nvidia After an unusual two-year Arm-based server CPU that it rolled out its first mid-range delay, silicon for a new Wi-Fi claims outperforms rivals and GPU based on the company's standard is starting to emerge. servers using it. The Kunpeng Turing GPU architecture, Over the next few months, a 920 shows the increasing promising to make available to handful of startups will sample sophistication of China's more PC gamers real-time chips for 802.11ah, a 900largest system vendor and ray-tracing graphics capability MHz version of Wi-Fi targeting chip designer at a time when in a card that retails for just long-range links especially for it's at the center of heated \$349. the internet of things. trade tensions with the U.S. read more read more read more FutureHorizons TALK TO US Quantum Dots to Shrink **Open-Silicon & SiFive to** MicroLED Display Pixels Chisel RISC-V in India **EVENTS** LONDON — Nanoco Silicon Chip Industry Technologies and Plessey **BENGALURU** - When Naveed Semiconductors have Seminar Sherwani and Satya Gupta partnered to shrink the pixel founded Open-Silicon, a 11 March 2019 - London UK size of monolithic microLED semiconductor solutions company in 2003 with the idea displavs using Nanoco's Industry Forecast Briefing cadmium-free quantum-dot of an open model in the (CFQD quantum dots) - 17 Sept 2019 - London UK hardware world, a free and semiconductor nanoparticle open instruction set DON'T MISS OUT.architecture (ISA) called technology BOOK NOW BY **RISC-V** was nowhere on the horizon CALLING +44 1732 740440 read more read more OR EMAIL mail@futuraharizana aam

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### Nvidia Turing GPU Brings Ray-Tracing In Real Time

SAN FRANCISCO — Nvidia rolled out its first mid-range GPU based on the company's Turing GPU architecture, promising to make available to more PC gamers real-time ray-tracing graphics capability in a card that retails for just \$349.

The GeForce RTX 2060, introduced by Nvidia CEO Jensen Huang during the company's pre-CES event in Las Vegas on Sunday night, features 6 GB of GDDR6 memory and 240 Tensor cores capable of delivering 52 teraflops of deeplearning compute. Nvidia says that the horsepower can improve the gaming performance of the RTX 2060 through an Nvidia feature known as Deep Learning Super Sampling (DLSS).

Nvidia maintains that the RTX 2060 is 60% faster on current titles than its previous-generation GTX 1060, Nvidia's most popular GPU.

#### **Startups Reinventing Wi-Fi for IoT**

SAN JOSE, Calif. — After an unusual two-year delay, silicon for a new Wi-Fi standard is starting to emerge. Over the next few months, a handful of startups will sample chips for 802.11ah, a 900-MHz version of Wi-Fi targeting long-range links especially for the internet of things.

The so-called HaLow products promise delivery of up to Mbits/s over distances of tens of meters to a kilometer and support for thousands of nodes on an access point. They will occupy a space between ultra-low-power and -cost LoRa and Sigfox networks and below more power-hungry LTE Cat-M and Narrowband-IoT networks that come with data plans.

#### Huawei Rolls 7-nm Arm Server CPU

SAN JOSE, Calif. — Huawei announced a 7-nm Arm-based server CPU that it claims outperforms rivals and servers using it. The Kunpeng 920 shows the increasing sophistication of China's largest system vendor and chip designer at a time when it's at the center of heated trade tensions with the U.S.

The Kunpeng 920 packs 64 custom Arm-v8 cores running at 2.6 GHz. It supports eight DDR4 channels running up to 2,933 MT/s, two 100G Ethernet ports, and PCIe Gen 4.

Huawei said that the chip hits 930 on a SpecInt benchmark. It claimed that this puts it 30% ahead in performance and 30% in power efficiency over Arm server rivals such as Marvell's ThunderX2 and Ampere's eMAG, recently adopted by Huawei's server rival Lenovo.

## **Quantum Dots To Shrink Microled Display Pixels**

LONDON — Nanoco Technologies and Plessey Semiconductors have partnered to shrink the pixel size of monolithic microLED displays using Nanoco's cadmium-free quantum-dot (CFQD quantum dots) semiconductor nanoparticle technology.

Using its existing gallium nitride (GaN)-on-silicon monolithic process, Plessey will integrate the CFQD quantum dots into selected regions of blue LED wafers to add red and green light, shrinking the smallest practical pixel size from today's 30  $\mu$ m to 4  $\mu$ m, a reduction of 87%. The process will enable the production of smaller, higher-resolution microLED displays in applications such as AR/VR devices, watches, and mobile devices while enhancing both color rendition and energy efficiency.

Speaking to EE Times from CES, the companies said that the partnership brings together two sets of expertise to address the color conversion needs of microLED customers — Nanoco with its expertise in manufacturing quantum dots at scale and Plessey for its microLED displays. The key challenge was being able to pattern the quantum dots appropriately on the photoresist and making sure the quantum dots were compatible with other materials used in the manufacturing process for the displays, they said.

#### **Open-Silicon & SiFive to Chisel RISC-V in India**

BENGALURU - When Naveed Sherwani and Satya Gupta founded Open-Silicon, a semiconductor solutions company in 2003 with the idea of an open model in the hardware world, a free and open instruction set architecture (ISA) called RISC-V was nowhere on the horizon.

Fifteen years later and after its acquisition by SiFive last year, it was just natural that RISC-V, which has created a disruption in the industry with its open model, would pave the way forward for this Bangalore-based company, also known for its OpenModel custom SoCs.

Open-Silicon's "OpenModel" was one of the first semiconductor industry's first end-to-end custom SoC development solution based on a revolutionary business model that provides a seamless, low-cost, and low-risk alternative to traditional models for complex ASIC design and development.