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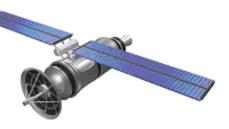## The Global Semiconductor Industry Analysts

FH MONDAY

DuPont Opens New Aerospace Technology Center

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AMD Chips to Power Exascale System

AMD snagged a design win for next-generation CPUs, GPUs, and interconnects in Frontier, the second of three U.S. exascale-class supercomputers. The total contract, valued at more than $600 million, is the largest deal to date for AMD, system integrator Cray, and the U.S. Department of Energy.

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DuPont Opens New Aerospace Technology Center

DuPont Transportation & Advanced Polymers opened a new Aerospace Technology Center at the DuPont Vespel manufacturing site in Valley View, Ohio. The site includes both a design showcase and collaboration spaces that aims to help engine designers to drive toward the goals of improved fuel efficiency, reduced noise, and reduced emissions.

"Innovation and collaboration are the keys to meeting the rapid pace of change and growth in the aerospace industry," said Randy Stone, President, DuPont Transportation & Advanced Polymers. "We are excited to welcome our customers and suppliers to this new Technology Center to help them experience firsthand how new advances in technology can help us all capitalize on new opportunities and drive industry growth."

AI Aids in the Detection of IEDs at Checkpoints

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Traditional X-ray machine with integrated AI. This Synapse technology is currently being used at a number of airports. Photo courtesy: Synapse Technology.

The initiative will be moderated through the Air Force's AFWERX program. "We look forward to collaborating closely with government agencies to solve their core use cases and help increase safety for our country's soldiers," said Madeline Zimmerman, business development lead for Synapse Technology.

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In an exclusive interview with EE Times, Imagination told us ray tracing will enable state-of-the-art realistic image rendering using light-modelling techniques to be integrated into graphics processing units (GPUs) across mobile, automotive, server and other markets. In describing its thinking and reasoning behind opening up its IP to external licensing, I got the sense that it wants to do for 5G what Arm did for previous generations of mobile applications: be the go-to IP for high performance graphics rendering. The company believes we are at an inflexion point similar to the one Arm experienced back in the 2000s.

3D Rocket Factory Takes Flight

A rocket manufacturer pioneering 3D printing technology is scheduled to loft its first payload into orbit as early as 2021 under terms of a deal between the aerospace startup and a launch services provider.

Relativity Space said this week it has signed a launch services agreement with Spaceflight, a satellite rideshare and mission management specialist. The deal calls for Spaceflight to book satellite launches to low-earth orbit using Relativity's Terran 1 rocket, an entirely 3D-printed rocket.

Relativity bills itself as the aerospace industry's first autonomous rocket factory and launch service integrating machine learning and intelligent robotics with 3D autonomous manufacturing technology. The startup claims it can build a rocket using additive manufacturing techniques in less than 60 days and initially loft payloads as large as 1,250 kg (2,755 pounds).

AMD Chips to Power Exascale System

SAN JOSE, Calif. — AMD snagged a design win for next-generation CPUs, GPUs, and interconnects in Frontier, the second of three U.S. exascale-class supercomputers. The total contract, valued at more than $600 million, is the largest deal to date for AMD, system integrator Cray, and the U.S. Department of Energy.

When installed sometime in 2021, Frontier is expected to deliver more than 1.5 exaflops, making it slightly higher in performance than Aurora, the first U.S. exascale system being built by Intel and Cray. A third system, dubbed El Capitan, is expected to be awarded to the team of IBM and Nvidia, who built Summit and Sierra, the current leading supercomputers.

The deal is a landmark for AMD's renewed focus on high-performance chips. To date, Intel has dominated as much as 95% of the CPU sockets in top supercomputers, with IBM's Power chips taking a significant slice of what remained.