



Jean-Marc Chery Biography

STMicroelectronics

Jean-Marc Chery is Chief Technology Officer of STMicroelectronics and has held this position since January 2008. His responsibilities include Corporate Technology R&D and the production at the Company's 12" wafer fab in Crolles. Chery is a member of ST's Corporate Executive Committee and sits on the Board of Directors of ST-Ericsson.

Chery began his career in the Quality organization at Matra, the French engineering group. He joined Thomson Semiconducteurs, a predecessor company to STMicroelectronics, in 1986. Chery worked as Planning and Front-End Production Control Manager and later as Operation Manager in Tours, France. In 2001, he moved to Central Manufacturing as GM of ST's wafer fabs in Rousset, France. In 2005, Chery led the company-wide restructuring program for 6" front-end wafer manufacturing. In 2006, he took charge of ST's Front-End Manufacturing and EWS (electrical wafer sort) operations in Asia Pacific.

Chery is a member of the CATRENE Support Group and sits on the Board of Directors of the Science and Engineering Research Council of the Singapore Agency for Science Technology And Research (A*STAR).

Jean-Marc Chery was born in Orleans, France, in 1960, and graduated with a degree in Engineering from ENSAM (Ecole Nationale Supérieure d'Arts et Métiers), Paris, France.

